AM/FM 1 CHIP RADIO KA22426D

INTRODUCTION

The KA22426D is a monolithic integrated circuit designed for radio-cassette tape recorders, clock radios and headphone radios.

FUNCTIONS

AM/FM RF AMP

Local OSC

AM AGC Control

FM AFC Control

· Audio Power AMP

Tuning Indicator

DC Volume

AM/FM IF AMP

FM Quadrature DET

AM DET

FEATURES

Built-in AM/FM Switching Circuit

Wide operating supply voltage: V_{CC} = 2V ~ 7.5V

Low current consumption (V_{CC} = 3V)

— FM: I_{CCQ} = 5.3 mA (Typ)

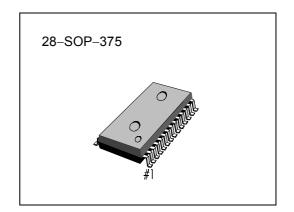
— AM: $I_{CCO} = 3.4 \text{ mA (Typ)}$

High Power Audio Amplifier: 0.5W (typ) at V_{CC} = 6V,

 $R_L = 8\Omega$, THD = 10%

ORDERING INFORMATION

Device	Package	Operating Temperature		
KA22426D	28-SOP-375	−20°C ~ +70°C		



KA22426D AM/FM 1 CHIP RADIO

BLOCK DIAGRAM

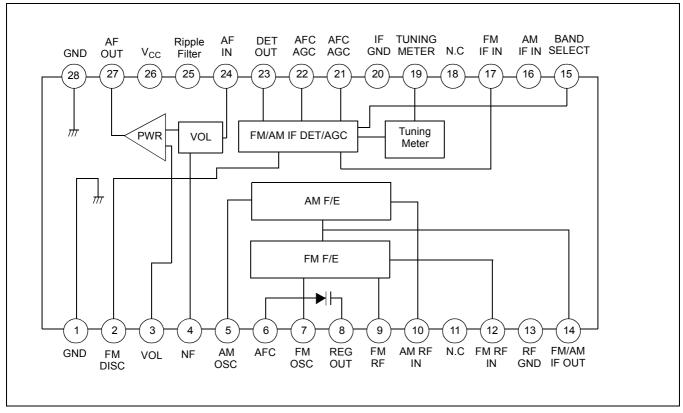


Figure 1.

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Characteristic	Symbol	Value	Unit
Supply Voltage	V _{CC}	9	V
Power Dissipation	P_{D}	1000	mW
Operating Temperature	T _{OPR}	−20 ~ +70	°C
Storage Temperature	T _{STG}	−40 ~ +125	°C

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ELECTRICAL CHARACTERISTICS

(V_{CC} = 6V, Ta = 25°C, FM; f = 22.5kHz, fm = 1kHz, AM; 30% Mod, unless otherwise specified)

	Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
FM	Quiescent Circuit Current	I _{CCQ}	V _I = 0	_	7.0	14.0	mA
	F/E Voltage Gain	G _{V1V}	$V_{I}(1) = 40 dB\mu, f_{C} = 100 MHz, \Delta f = 0$	32	39	46	dB
	Detect Output Gain	V _O (1)	$V_{I}(3) = 90dB\mu, f_{i} = 10.7MHz$	-26	-20	-14	dBm
	IF-3 dB Sensitivity	V _{I (LIM)}	$V_O(VI3) = 90dB\mu$, -3 dB, $f_i = 10.7MHz$	-	24	32	dBμ
	Total Harmonic Distortion	THD ₁	$V_{I}(3) = 90dB\mu, f_{I} = 10.7MHz(\Delta f = 75kHz)$	-	0.3	2.0	%
	Meter Drive Current	I _M (1)	$V_{I}(3) = 60dB\mu, f_{I} = 10.7MHz$	1.8	3.5	7.0	mA
AM	Quiescent Circuit Current	I _{CCQ} (2)	V _I = 0	-	3.5	10.0	mA
	F/E Voltage Gain	G _V (2)	$V_{I}(2) = 60 dB\mu$, $f_{C} = 1660 kHz$, $m = 0%$	15	22	29	dB
	IF Voltage Gain	G _V (3)	$V_{O}(3) = -34$ dBm, $f_{i} = 455$ kHz	14	20	27	dBμ
	AM Detect Output Voltage	V _O (2)	$V_{I}(3) = 85dB\mu$, $f_{i} = 455kHz$	-26	-20	-14	dBm
	Total Harmonic Distortion	THD ₂	$V_{I}(2) = 95 dB\mu$, $f_{c} = 1660 kHz$, $Vcc = 7.8V$	_	0.6	2.0	%
	Meter Drive Current	I _M (2)	$V_{I}(3) = 85dB\mu, f_{i} = 455kHz$	1.3	3.0	7.0	mA
AF	Closed Loop Voltage Gain	G _V (4)	V _O (4) = 0dBm, f = 1kHz	27	31.5	36	dB
	Total Harmonic Distortion	THD ₃	P _O = 50mW, f = 1kHz	-	0.3	2.5	%
	Output Power	Po	$R_L = 8\Omega$, THD = 10%, f = 1kHz	0.4	0.5	_	W



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APPLICATION CIRCUIT

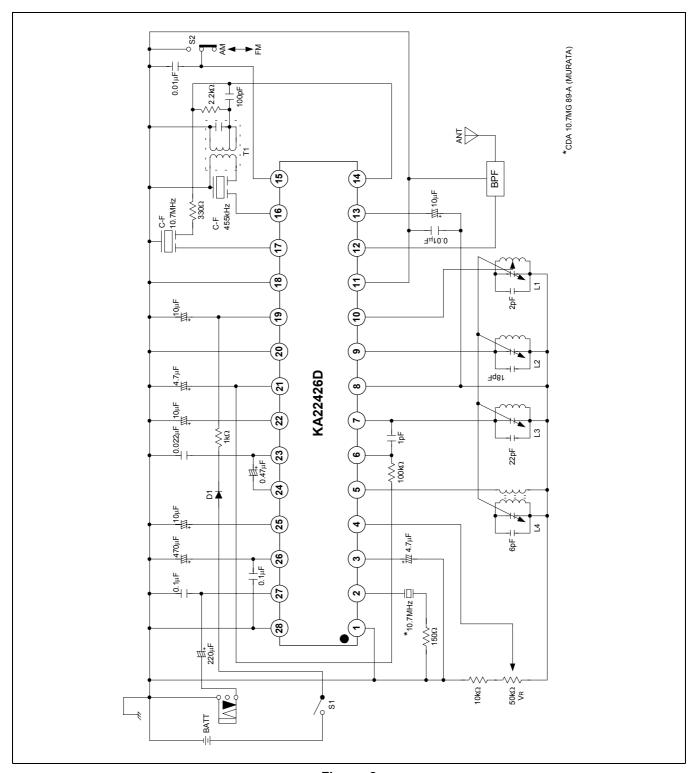


Figure 2.



This datasheet has been download from:

www.datasheetcatalog.com

Datasheets for electronics components.